FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 120073.415D1	APPLICATION NO.
INFORMATION DISCLOSURE STATEMENT *(Use several sheets if necessary)*		APPLICANTS Michael R. Buckmaster et al.	
		FILING DATE July 25, 2001	GROUP ART UNIT

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY		TRANSLATION YES	NO
	AK						
	AL						

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

RG	AM	Manku et al., "Circuit Partitioning with Partial Order for Mixed Simulation Emulation Environment," Sixth IEEE International Workshop on Rapid System Prototyping, pp. 201-207. June 1995.
RG	AN	Borgatti et al., "A Smoothly Upgradable Approach to Virtual Emulation of HW/SW Systems," Proceedings of the Seventh International Workshop on Rapid System Prototyping, pp. 83-88. June 1996.
RG	AO	Bauer et al., "Hardware/Software Co-Simulation in a VHDL-based Test Bench Approach," IEEE Proceedings of the 34th Design Automation Conference, pp. 774-779. June 1996.
RG	AP	Berger et al., "Co-Verification Handles More Complex Embedded Systems," Electronic Design, pp. 96-104. September 15, 1997.

EXAMINER	DATE CONSIDERED 3/30/05

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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